

10075113
02/13/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10075113	02/13/2002	718 324	718	2133 2850	DeCady

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R50

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

FRANCE 0102333 02/21/2001

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

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TITLE : Method of testing a sequential access memory plane and a corresponding sequential access memory semiconductor device

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

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NOTICE OF ALLOWANCE MAILED

CLAIMS ALLOWED

Assistant Examiner

Total Claims

Print Claim for
O.G

ISSUE FEE

Amount Due

Date Paid

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

Primary Examiner

PREPARED FOR ISSUE

Application Examiner



TERMINAL

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